

APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date:: 12/31/03
Application Type:: Regular
Subject Matter:: Utility
Title:: Automatic Antenna Diode Insertion
for Integrated Circuits
Attorney Docket Number:: X-1294 US
Request for Early Pub?:: No
Request for Non-Pub?:: Yes
Total Drawing Sheets:: dwg
Small Entity?:: No
Petition included?:: No

APPLICANT INFORMATION

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Andy
Middle Name:: H.
Family Name:: Gan
Street:: 1055 Scaletta Lane
City:: San Jose
State or Province:: CA
Postal or Zip Code:: 95120

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: United Kingdom
Status:: Full Capacity
Given Name:: Nigel
Middle Name:: G.
Family Name:: Herron
Street:: 4628 Fallstone Court
City:: San Jose

State or Province:: CA
Postal or Zip Code:: 95124

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

Representative Customer Number::	24309	
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ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.
Street:: 2100 Logic Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95124